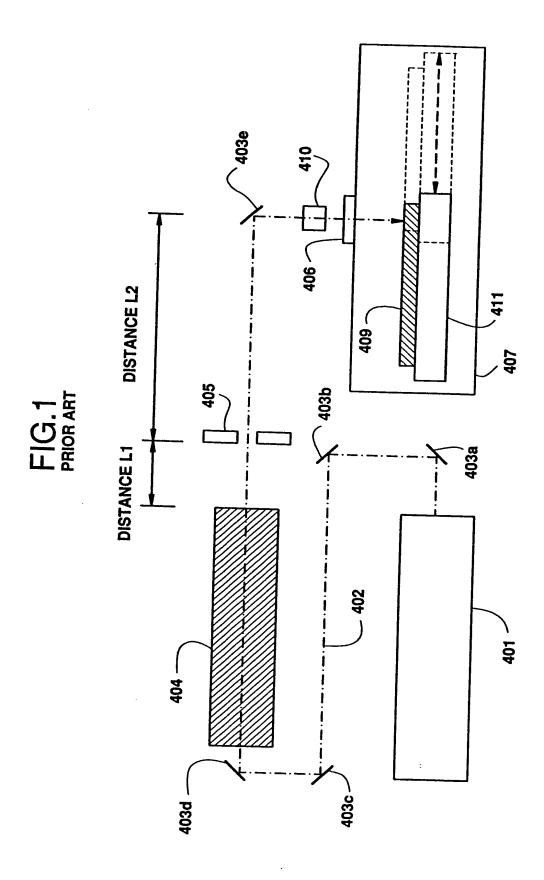
Inventors' Names: Hiroshi TANABE, et al.

Title: Semiconductor Manufacturing Apparatus and
Manufacturing Method of Thin Film Semiconductor
Device

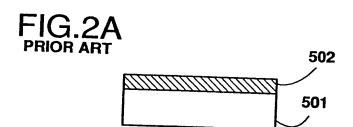
Appln. No.: Div. of 08/866,331 Filed: 10/01/2003

Group Art Unit: 2822 (Parent) Sughrue Ref. No.: Q77312

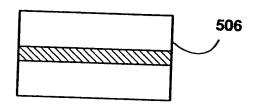
Sughrue Telephone No.: (202) 293-7060
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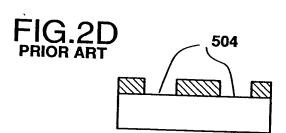


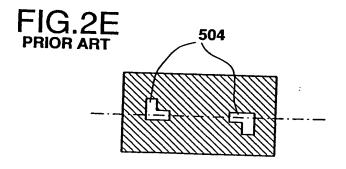
## FIG.2B PRIOR ART



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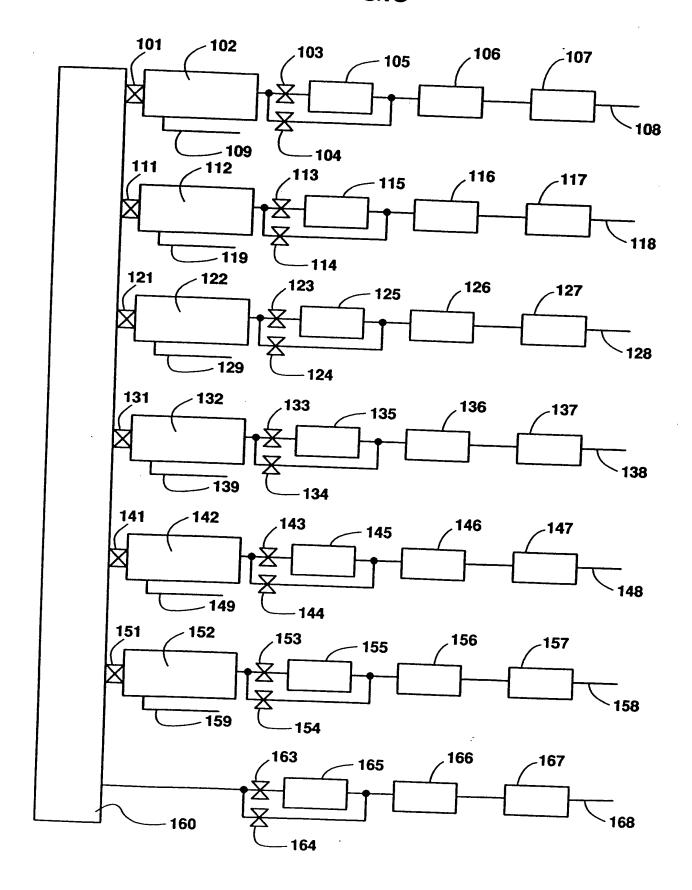






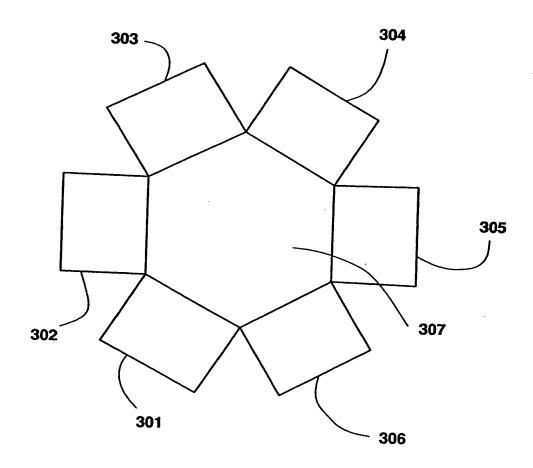
Appln. No.: Div. of 08/866,331 Filed: 10/01/2003
Group Art Unit: 2822 (Parent) Sughrue Ref. No.: Q77312
Sughrue Telephone No.: (202) 293-7060
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## FIG.3

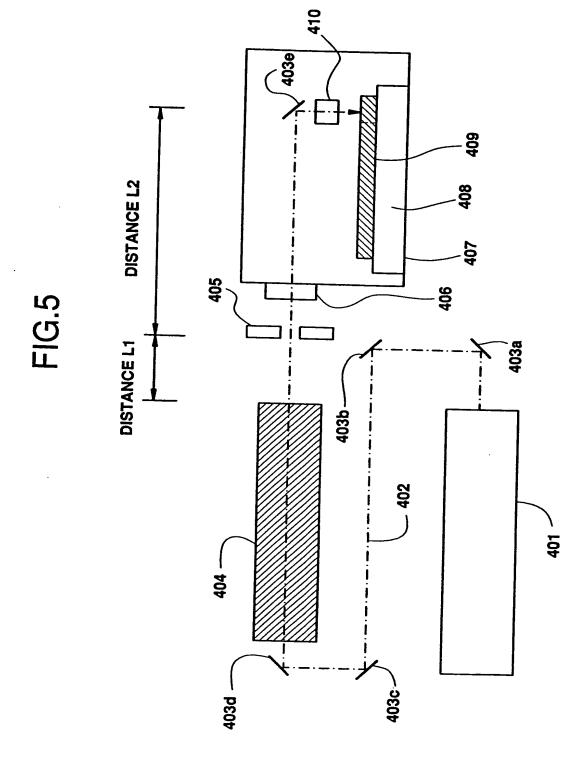


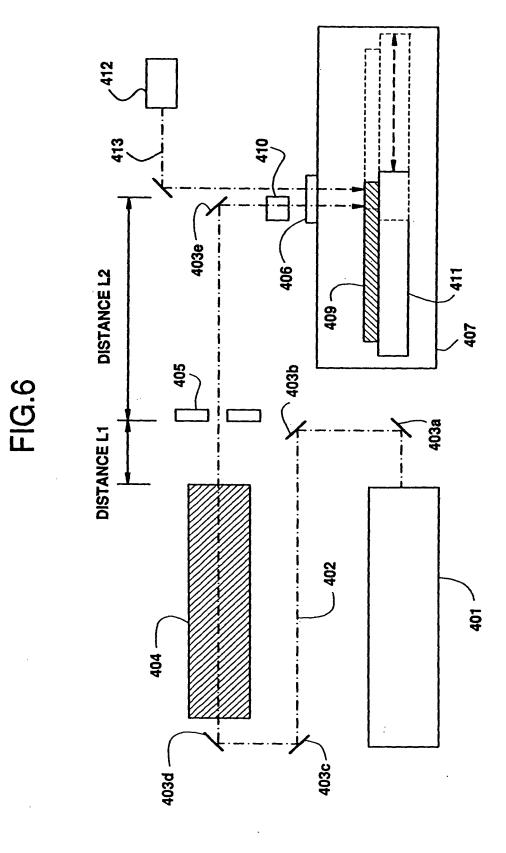
Device
Appln. No.: Div. of 08/866,331 Filed: 10/01/2003
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## FIG.4



Manufactum<sub>6</sub> .....
Device
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FIG.7A

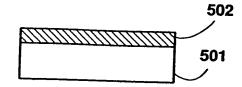
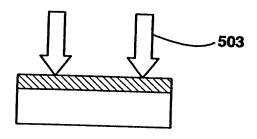
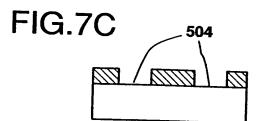
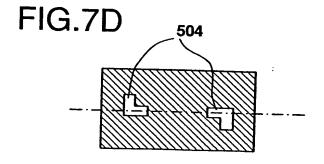


FIG.7B



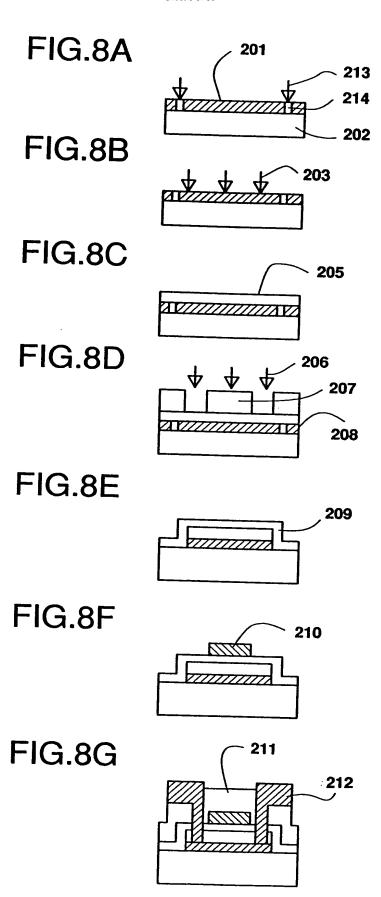




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FIG.9A

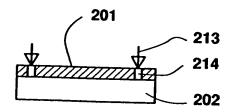


FIG.9B

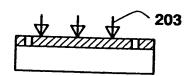


FIG.9C

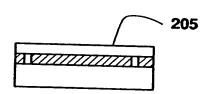


FIG.9D

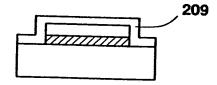


FIG.9E

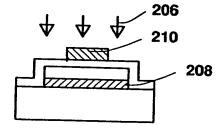
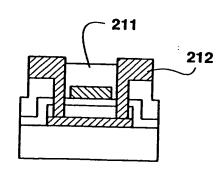


FIG.9F



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